

Thomas Lee Cmos Rf Solution Manual Cambridge

It is hardly a profound observation to note that we remain in the midst of a wireless revolution. In 1998 alone, over 150 million cell phones were sold worldwide, representing an astonishing 50% increase over the previous year. Maintaining such a remarkable growth rate requires constant innovation to decrease cost while increasing performance and functionality. Traditionally, wireless products have depended on a mixture of semiconducting technologies, spanning GaAs, bipolar, and BiCMOS, just to name a few. A question that has been hotly debated is whether CMOS could ever be suitable for RF applications. However, given the acknowledged inferiority of CMOS transistors relative to those in other candidate technologies, it has been argued by many that “CMOS RF” is an oxymoron, an endeavor best left cloistered in the ivory towers of academia. In rebuttal, there are several compelling reasons to consider CMOS for wireless applications. Aside from the exponential device and density improvements delivered regularly by Moore’s law, only CMOS offers a technology path for integrating RF and digital elements, potentially leading to exceptionally compact and low-cost devices. To enable this achievement, several thorny issues need to be resolved. Among these are the problem of poor passive components, broadband noise in MOSFETs, and phase noise in oscillators made with CMOS. Beyond the component level, there is also the important question of whether there are different architectural choices that one would make if CMOS were used, given the different constraints. This book, first published in 2004, is an expanded and revised edition of Tom Lee’s acclaimed RFIC text.

Pozar’s new edition of Microwave Engineering includes more material on active circuits, noise, nonlinear effects, and wireless systems. Chapters on noise and nonlinear distortion, and active devices have been added along with the coverage of noise and more material on intermodulation distortion and related nonlinear effects. On active devices, there’s more updated material on bipolar junction and field effect transistors. New and updated material on wireless communications systems, including link budget, link margin, digital modulation methods, and bit error rates is also part of the new edition. Other new material includes a section on transients on transmission lines, the theory of power waves, a discussion of higher order modes and frequency effects for microstrip line, and a discussion of how to determine unloaded.

A transistor-level, design-intensive overview of high speed and high frequency monolithic integrated circuits for wireless and broadband systems from 2 GHz to 200 GHz, this comprehensive text covers high-speed, RF, mm-wave, and optical fibre circuits using nanoscale CMOS, SiGe BiCMOS, and III-V technologies. Step-by-step design methodologies, end-of chapter problems, and practical simulation and design projects are provided, making this an ideal resource for senior undergraduate and graduate courses in circuit design. With an emphasis on device-circuit topology interaction and optimization, it gives circuit designers and students alike an in-depth understanding of device structures and process limitations affecting circuit performance.

Microwave Journal

Semiconductor Devices and Technologies for Future Ultra Low Power Electronics

From Fundamentals to Applications

Microwave Engineering

3D Microelectronic Packaging

The Design of CMOS Radio-Frequency Integrated CircuitsCambridge University Press

Discover the concepts and techniques needed to design millimeter-wave circuits for current and emerging wireless system applications.

This book provides an introduction to the basic concepts in digital communications for readers with little or no previous exposure to either digital or analog communications. The intent is to help learners develop a firm understanding of digital communication system engineering—and to enable them to conduct system-level design and analysis for digital communication systems of the future. As a result, the book emphasizes the basic principles of digital communications theory and techniques, rather than presenting specific technologies for implementation. Chapter topics include probability and random variables—review and notation, introduction to random processes, linear filtering of random processes, frequency-domain analysis of random processes in linear systems, baseband transmission of binary data, coherent communications, noncoherent communications, intersymbol interference, and spread-spectrum communication systems. For individuals preparing for a career in wireless communications system design.

Demand for wireless local area network systems has led to new frequency bands and new standards to accommodate higher data rates. Moreover, opportunities are increasing for the development of low-cost integrated WLAN systems. This guide for RF and high-speed analog circuit designers and students as well as wireless engineers studies the phase-locked loop as a basic building block of frequency synthesizers and WLAN receivers. It provides guidelines and engineering solutions for the design of loop filters in high-frequency PLLs. Rategh (Tavanza Inc.) and Lee (Stanford U.) discuss the different analog and digital frequency division techniques and introduce injection-locked frequency dividers as an alternative to conventional frequency dividers. c. Book News Inc.

The VLSI Handbook

A Practical Guide to Theory, Measurement, and Circuits

Nano-CMOS Circuit and Physical Design

The Design of CMOS Radio-Frequency Integrated Circuits

Multi-GHz Frequency Synthesis & Division

This book covers the fundamentals and significance of 2-D materials and related semiconductor transistor technologies for the next-generation ultra low power applications. It provides comprehensive coverage on advanced low power transistors such as NCFETs, FinFETs, TFETs, and flexible transistors for future ultra low power applications owing to their better subthreshold swing and scalability. In addition, the text examines the use of field-effect transistors for biosensing applications and covers design considerations and compact modeling of advanced low power transistors such as NCFETs, FinFETs, and TFETs. TCAD simulation examples are also provided. FEATURES Discusses the latest updates in the field of ultra low power semiconductor transistors Provides both experimental and analytical solutions for TFETs and NCFETs Presents synthesis and fabrication processes for FinFETs Reviews details on 2-D materials and 2-D transistors Explores the application of FETs for biosensing in the healthcare field This book is aimed at researchers, professionals, and graduate students in electrical engineering, electronics and communication engineering, electron devices, nanoelectronics and nanotechnology, microelectronics, and solid-state circuits.

Improving the performance of the power amplifier is the most pressing problem facing designers of modern radio-frequency (RF) transceivers. Linearity and power efficiency of the transmit path are of utmost importance, and the power amplifier has proven to be the bottleneck for both. High linearity enables transmission at the highest data rates for a given channel bandwidth, and power efficiency prolongs battery lifetime in portable units and reduces heat dissipation in high-power transmitters. Cartesian feedback is a power amplifier linearization technique that acts to soften the tradeoff between power efficiency and linearity in power amplifiers. Despite its compelling, fundamental advantages, the technique has not enjoyed widespread acceptance because of certain implementation difficulties. Feedback Linearization of RF Power Amplifiers introduces new techniques for overcoming the challenges faced by the designer of a Cartesian feedback system. The theory of the new techniques are described and analyzed in detail. The book culminates with the results of the first known fully integrated Cartesian feedback power amplifier system, whose design was enabled by the techniques described. Feedback Linearization of RF Power Amplifiers is a valuable reference work for engineers in the telecommunications industry, industry researchers, academic researchers.

This volume provides a comprehensive reference for graduate students and professionals in both academia and industry on the fundamentals, processing details, and applications of 3D microelectronic packaging, an industry trend for future microelectronic packages. Chapters written by experts cover the most recent research results and industry progress in the following areas: TSV, die processing, micro bumps, direct bonding, thermal compression bonding, advanced materials, heat dissipation, thermal management, thermal mechanical modeling, quality, reliability, fault isolation, and failure analysis of 3D microelectronic packages. Numerous images, tables, and didactic schematics are included throughout. This essential volume equips readers with an in-depth understanding of all aspects of 3D packaging, including packaging architecture, processing, thermal mechanical and moisture related reliability concerns, common failures, developing areas, and future challenges, providing insights into key areas for future research and development.

This book provides analysis and discusses the design of various MOSFET technologies which are used for the design of Double-Pole Four-Throw (DP4T) RF switches for next generation communication systems. The authors discuss the design of the (DP4T) RF switch by using the Double-Gate (DG) MOSFET, as well as the Cylindrical Surrounding double-gate (CSDG) MOSFET. The effect of HFO2 (high dielectric material) in the design of DG MOSFET and CSDG MOSFET is also explored. Coverage includes comparison of Single-gate MOSFET and Double-gate MOSFET switching parameters, as well as testing of MOSFETs parameters using image acquisition.

From Circuit Level to Architecture Level

EDA, Design and Microarchitectures

Feedback Linearization of RF Power Amplifiers

Proceedings

MOSFET Technologies for Double-Pole Four-Throw Radio-Frequency Switch

In chapters culled from popular and critically acclaimed Electromagnetic Compatibility Handbook, Electromagnetic Shielding provides a tightly focused, convenient, and affordable reference for those interested primarily in this subset of topics. Author Kenneth L. Kaiser demystifies shielding and explains the source and limitations of the approximations, guidelines, models, and rules-of-thumb used in this field. The material is presented in a unique question-and-answer format that gets straight to the heart of each topic. The book includes numerous examples and uses Mathcad to generate all of the figures and many solutions to equations. In many cases, the entire Mathcad program is provided.

Based on the authors’ expansive collection of notes taken over the years, Nano-CMOS Circuit and Physical Design bridges the gap between physical and circuit design and fabrication processing, manufacturability, and yield. This innovative book covers: process technology, including sub-wavelength optical lithography; impact of process scaling on circuit and physical implementation and low power with leaky transistors; and DFM, yield, and the impact of physical implementation.

We live in a time of great change. In the electronics world, the last several decades have seen unprecedented growth and advancement, described by Moore’s law. This observation stated that transistor density in integrated circuits doubles every 1.5–2 years. This came with the simultaneous improvement of individual device performance as well as the reduction of device power such that the total power of the resulting ICs remained under control. No trend remains constant forever, and this is unfortunately the case with Moore’s law. The trouble began a number of years ago when CMOS devices were no longer able to proceed along the classical scaling trends. Key device parameters such as gate oxide thickness were simply no longer able to scale. As a result, device on-state currents began to creep up at an alarming rate. These continuing problems with classical scaling have led to a leveling off of IC clock speeds to the range of several GHz. Of course, chips can be clocked higher but the thermal issues become unmanageable. This has led to the recent trend toward microprocessors with multiple cores, each running at a few GHz at the most. The goal is to continue improving performance via parallelism by adding more and more cores instead of increasing speed. The challenge here is to ensure that general purpose codes can be efficiently parallelized. There is another potential solution to the problem of how to improve CMOS technology performance: three-dimensional integrated circuits (3D ICs).

High Mobility Materials for CMOS Applications provides a comprehensive overview of recent developments in the field of (Si)Ge and III-V materials and their integration on Si. The book covers material growth and integration on Si, going all the way from device to circuit design. While the book’s focus is on digital applications, a number of chapters also address the use of III-V for RF and analog applications, and in optoelectronics. With CMOS technology moving to the 10nm node and beyond, however, severe concerns with power dissipation and performance are arising, hence the need for this timely work on the advantages and challenges of the technology. Addresses each of the challenges of utilizing high mobility materials for CMOS applications, presenting possible solutions and the latest innovations Covers the latest advances in research on heterogeneous integration, gate stack, device design and scalability Provides a broad overview of the topic, from materials integration to circuits

EDA for IC Implementation, Circuit Design, and Process Technology

Electronic Design Automation for IC System Design, Verification, and Testing

From VLSI Architectures to CMOS Fabrication

Design of Analog CMOS Integrated Circuits

RF Circuit Design

Modern wireless communications hardware is underpinned by RF and microwave design techniques. This insightful book contains a wealth of circuit layouts, design tips, and practical measurement techniques for building and testing practical gigahertz systems. The book covers everything you need to know to design, build, and test a high-frequency circuit. Microstrip components are discussed, including tricks for extracting good performance from cheap materials. Connectors and cables are also described, as are discrete passive components, antennas, low-noise amplifiers, oscillators, and frequency synthesizers. Practical measurement techniques are presented in detail, including the use of network analyzers, sampling oscilloscopes, spectrum analyzers, and noise figure meters. Throughout the focus is practical, and many worked examples and design projects are included. There is also a CD-ROM that contains a variety of design and analysis programs. The book is packed with indispensable information for students taking courses on RF or microwave circuits and for practising engineers.

This volume of Analog Circuit Design concentrates on three topics: Volt Electronics; Design and Implementation of Mixed-Mode Systems; Low-Noise and RF Power Amplifiers for Telecommunication. The book comprises six papers on each topic written by internationally recognised experts. These papers are tutorial in nature and together make a substantial contribution to improving the design of analog circuits. The book is divided into three parts: Part I, Volt Electronics, presents some of the circuit design challenges which are having to be met as the need for more electronics on a chip forces smaller transistor dimensions, and thus lower breakdown voltages. The papers cover techniques for 1-Volt electronics. Part II, Design and Implementation of Mixed-Mode Systems, deals with the various problems that are encountered in mixed analog-digital design. In the future, all integrated circuits are bound to contain both digital and analog sub-blocks. Problems such as substrate bounce and other substrate coupling effects cause deterioration in signal integrity. Both aspects of mixed-signal design have been addressed in this section and it illustrates that careful layout techniques embedded in a hierarchical design methodology can allow us to cope with most of the challenges presented by mixed analog-digital design. Part III, Low-noise and RF Power Amplifiers for Telecommunication, focuses on telecommunications systems. In these systems low-noise amplifiers are front-ends of receiver designs. At the transmitter part a high-performance, high-efficiency power amplifier is a critical design. Examples of both system parts are described in this section. Analog Circuit Design is an essential reference source for analog design engineers and researchers wishing to keep abreast with the latest developments in the field. The tutorial nature of the contributions also makes it suitable for use in an advanced course.

This modern, pedagogic textbook from leading author Behzad Razavi provides a comprehensive and rigorous introduction to CMOS PLL design, featuring intuitive presentation of theoretical concepts, extensive circuit simulations, over 200 worked examples, and 250 end-of-chapter problems. The perfect text for senior undergraduate and graduate students.

A Must-Read for all RF/RFIC Circuit Designers This book targets the four most difficult skills facing RF/RFIC designers today: impedance matching, RF/AC grounding, Six Sigma design, and RFIC technology. Unlike most books on the market, it presents readers with practical engineering design examples to explore how they’re used to solve ever more complex problems. The content is divided into three key parts: Individual RF block circuit design Basic RF circuit design skills RF system engineering The author assumes a fundamental background in RF circuit design theory, and the goal of the book is to enable readers to master the correct methodology. The book includes treatment of special circuit topologies and introduces some useful schemes for simulation and layout. This is a must-read for RF/RFIC circuit design engineers, system designers working with communication systems, and graduates and researchers in related fields.

IEEE Circuits & Devices

Designing Bipolar Transistor Radio Frequency Integrated Circuits

3D IC and RF SiPs: Advanced Stacking and Planar Solutions for 5G Mobility

SiGe Heterojunction Bipolar Transistors

If you’re looking for an in-depth and up-to-date understanding bipolar transistor RFIC design, this practical resource is a smart choice. Unlike most books on the market that focus on GaAs MESFET or silicon CMOS process technology, this unique volume is dedicated exclusively to RFIC designs based on bipolar technology. Until now, critical GaAs HBT and SiGe HBT process technologies have been largely neglected in reference books. This book fills this gap, offering you a detailed treatment of this increasingly important topic. You discover a wide range of circuit topologies that are optimized for maximum performance with bipolar devices. From discussions of key applications (Bluetooth, UWB, GPS, WiMax) and architectures... to in-depth coverage of fabrication technologies and amplifier design... to a look at performance tradeoffs and production costs, this book arms you with complete design know-how for your challenging work in the field.

Oscillators are an important component in today’s RF and microwave systems, and practitioners in the field need to know how to design oscillators for stability and top performance. Offering engineers broader coverage than other oscillator design books on the market, this comprehensive resource considers the complete frequency range, from low-frequency audio oscillators to more complex oscillators found at the RF and microwave frequencies. Packed with over 1,200 equations, the book gives professionals a thorough understanding of the principles and practice of oscillator circuit design and emphasizes the use of time-saving CAD (computer aided design) simulation techniques. From the theory and characteristics of oscillators, to the design of a wide variety of oscillators (including tuned-circuit, crystal, negative-resistance, and relaxation oscillators), this unique book is a one-stop reference practitioners can turn to again and again when working on their challenging projects in this field.

This practical, tool-independent guide to designing digital circuits takes a unique, top-down approach, reflecting the nature of the design process in industry. Starting with architecture design, the book comprehensively explains the why and how of digital circuit design,

using the physics designers need to know, and no more.

For the new millenium, Wai-Kai Chen introduced a monumental reference for the design, analysis, and prediction of VLSI circuits: The VLSI Handbook. Still a valuable tool for dealing with the most dynamic field in engineering, this second edition includes 13 sections comprising nearly 100 chapters focused on the key concepts, models, and equations. Written by a stellar international panel of expert contributors, this handbook is a reliable, comprehensive resource for real answers to practical problems. It emphasizes fundamental theory underlying professional applications and also reflects key areas of industrial and research focus. WHAT'S IN THE SECOND EDITION? Sections on... Low-power electronics and design VLSI signal processing Chapters on... CMOS fabrication Content-addressable memory Compound semiconductor RF circuits High-speed circuit design principles SiGe HBT technology Bipolar junction transistor amplifiers Performance modeling and analysis using SystemC Design languages, expanded from two chapters to twelve Testing of digital systems Structured for convenient navigation and loaded with practical solutions, The VLSI Handbook, Second Edition remains the first choice for answers to the problems and challenges faced daily in engineering practice.

The Design and Implementation of Low-Power CMOS Radio Receivers

Digital Integrated Circuit Design

Foundations of Oscillator Circuit Design

Telecommunication Electronics

Conference Proceedings

Presenting a comprehensive overview of the design automation algorithms, tools, and methodologies used to design integrated circuits, the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes. The second volume, EDA for IC Implementation, Circuit Design, and Process Technology, thoroughly examines real-time logic to GDSII (a file format used to transfer data of semiconductor physical layout), analog/mixed signal design, physical verification, and technology CAD (TCAD). Chapters contributed by leading experts authoritatively discuss design for manufacturability at the nanoscale, power supply network design and analysis, design modeling, and much more. Save on the complete set.

This practical, hands-on resource describes functional units and circuits of telecommunication systems. The functions characterizing these systems, including RF amplifiers (both low noise and power amplifiers), signal sources, mixers and phase lock loops, are explored from an operational level viewpoint. And as all functions are migrating to digital implementations, this book describes functional units and circuits of telecommunication systems (with radio, wire, or optical links), from functional level viewpoint to the circuit details and examples. The structure of a radio transceiver is described and a view of all functional units, including migration to SDR (Software Defined Radio) is provided. Chapters include a functional identification of the units described and analysis of possible circuit solutions and analysis of error sources. The sequence reflects the actual design procedure: functional identification, search and analysis of solutions, and critical review to provide an understanding of the various solutions and tradeoffs, with guidelines for design and/or selection of proper functional units.

An interdisciplinary guide to enabling technologies for 3D ICs and 5G mobility, covering packaging, design to product life and reliability assessments Features an interdisciplinary approach to the enabling technologies and hardware for 3D ICs and 5G mobility Presents statistical treatments and examples with tools that are easily accessible, such as Microsoft's Excel and Minitab Fundamental design topics such as electromagnetic design for logic and RF/passives centric circuits are explained in detail Provides chapter-wise review questions and powerpoint slides as teaching tools

Sample Text

Transmission Lines, Matching, and Crosstalk

High Mobility Materials for CMOS Applications

Beyond 100 GHz

CMOS Digital Integrated Circuits

Software-Defined Radio for Engineers

This newly revised and expanded edition of the 2003 Artech House classic, Radio Frequency Integrated Circuit Design, serves as an up-to-date, practical reference for complete RFIC know-how. The second edition includes numerous updates, including greater coverage of CMOS PA design, RFIC design with on-chip components, and more worked examples with simulation results. By emphasizing working designs, this book practically transports you into the authors' own RFIC lab so you can fully understand the function of each design detailed in this book. Among the RFIC designs examined are RF integrated LC-based filters, VCO automatic amplitude control loops, and fully integrated transformer-based circuits, as well as image reject mixers and power amplifiers. If you are new to RFIC design, you can benefit from the introduction to basic theory so you can quickly come up to speed on how RFICs perform and work together in a communications device. A thorough examination of RFIC technology guides you in knowing when RFICs are the right choice for designing a communication device. This leading-edge resource is packed with over 1,000 equations and more than 435 illustrations that support key topics."

This textbook deals with the analysis and design of analog CMOS integrated circuits, emphasizing recent technological developments and design paradigms that students and practicing engineers need to master to succeed in today's industry. Based on the author's teaching and research experience in the past ten years, the text follows three general principles: (1) Motivate the reader by describing the significance and application of each idea with real-world problems; (2) Force the reader to look at concepts from an intuitive point of view, preparing him/her for more complex problems; (3) Complement the intuition by rigorous analysis, confirming the results obtained by the intuitive, yet rough approach.

SiGe HBTs is a hot topic within the microelectronics community because of its applications potential within integrated circuits operating at radio frequencies. Applications range from high speed optical networking to wireless communication devices. The addition of germanium to silicon technologies to form silicon germanium (SiGe) devices has created a revolution in the semiconductor industry. These transistors form the enabling devices in a wide range of products for wireless and wired communications. This book features: SiGe products include chip sets for wireless cellular handsets as well as WLAN and high-speed wired network applications Describes the physics and technology of SiGe HBTs, with coverage of Si and Ge bipolar transistors Written with the practising engineer in mind, this book explains the operating principles and applications of bipolar transistor technology. Essential reading for practising microelectronics engineers and researchers. Also, optical communications engineers and communication technology engineers. An ideal reference tool for masters level students in microelectronics and electronics engineering.

The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.

Millimeter-Wave Circuits for 5G and Radar

Radio Frequency Integrated Circuit Design

Design of CMOS Phase-Locked Loops

Frequency Synthesizer Design for 5 GHz Wireless LAN Systems

Analysis and Design

The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook, Second Edition, Electronic Design Automation for IC System Design, Verification, and Testing thoroughly examines system-level design, microarchitectural design, logic verification, and testing. Chapters contributed by leading experts authoritatively discuss processor modeling and design tools, using performance metrics to select microprocessor cores for integrated circuit (IC) designs, design and verification languages, digital simulation, hardware acceleration and emulation, and much more. New to This Edition: Major updates appearing in the initial phases of the design flow, where the level of abstraction keeps rising to support more functionality with lower non-recurring engineering (NRE) costs Significant revisions reflected in the final phases of the design flow, where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting-edge applications and approaches realized in the decade since publication of the previous edition—these are illustrated by new chapters on high-level synthesis, system-on-chip (SoC) block-based design, and back-annotating system-level models Offering improved depth and modernity, Electronic Design Automation for IC System Design, Verification, and Testing provides a valuable, state-of-the-art reference for electronic design automation (EDA) students, researchers, and professionals.

In chapters culled from the popular and critically acclaimed Electromagnetic Compatibility Handbook, Transmission Lines, Matching, and Crosstalk provides a tightly focused, convenient, and affordable reference for those interested primarily in this subset of topics. Author Kenneth L. Kaiser demystifies transmission lines, matching, and crosstalk and explains the source and limitations of the approximations, guidelines, models, and rules-of-thumb used in this field. The material is presented in a unique question-and-answer format that gets straight to the heart of each topic. The book includes numerous examples and uses Mathcad to generate all of the figures and many solutions to equations. In many cases, the entire Mathcad program is provided.

Based on the popular Artech House classic, Digital Communication Systems Engineering with Software-Defined Radio, this book provides a practical approach to quickly learning the software-defined radio (SDR) concepts needed for work in the field. This up-to-date volume guides readers on how to quickly prototype wireless designs using SDR for real-world testing and experimentation. This book explores advanced wireless communication techniques such as OFDM, LTE, WLA, and hardware targeting. Readers will gain an understanding of the core concepts behind wireless hardware, such as the radio frequency front-end, analog-to-digital and digital-to-analog converters, as well as various processing technologies. Moreover, this volume includes chapters on timing estimation, matched filtering, frame synchronization message decoding, and source coding. The orthogonal frequency division multiplexing is explained and details about HDL code generation and deployment are provided. The book concludes with coverage of the WLAN toolbox with OFDM beacon reception and the LTE toolbox with downlink reception. Multiple case studies are provided throughout the book. Both MATLAB and Simulink source code are included to assist readers with their projects in the field.

The book covers the CMOS-based millimeter wave circuits and devices and presents methods and design techniques to use CMOS technology for circuits operating beyond 100 GHz. Coverage includes a detailed description of both active and passive devices, including modeling techniques and performance optimization. Various mm-wave circuit blocks are discussed, emphasizing their design distinctions from low-frequency design methodologies. This book also covers a device-oriented circuit design technique that is essential for ultra high speed circuits and gives some examples of device/circuit co-design that can be used for mm-wave technology.

Ultra High-Speed CMOS Circuits

Three-Dimensional Integrated Circuit Design

Electromagnetic Shielding

Analog Circuit Design

High-Frequency Integrated Circuits

Summarizes the schemes and technologies in RF circuit design, describes the basic parameters of an RF system and the fundamentals of RF system design, and presents an introduction of the individual RF circuit block design. Forming the backbone of today's mobile and satellite communications networks, radio frequency (RF) components and circuits are incorporated into everything that transmits or receives a radio wave, such as mobile phones, radio, WiFi, and walkie talkies. RF Circuit Design, Second Edition immerses practicing and aspiring industry professionals in the complex world of RF design. Completely restructured and reorganized with new content, end-of-chapter exercises, illustrations, and an appendix, the book presents integral information in three complete sections: Part One explains the different methodologies between RF and digital circuit design and covers voltage and power transportation, impedance matching in narrow-band case and wide-band case, gain of a raw device, measurement, and grounding. It also goes over equipotentiality and current coupling on ground surface, as well as layout and packaging, manufacturability of product design, and radio frequency integrated circuit (RFIC). Part Two includes content on the main parameters and system analysis in RF circuit design, the fundamentals of differential pair and common-mode rejection ratio (CMRR), Balun, and system-on-a-chip (SOC). Part Three covers low-noise amplifier (LNA), power amplifier (PA), voltage-controlled oscillator (VCO), mixers, and tunable filters. RF Circuit Design, Second Edition is an ideal book for engineers and managers who work in RF circuit design and for courses in electrical or electronic engineering.

Planar Microwave Engineering

Introduction to Digital Communications

Volt Electronics; Mixed-Mode Systems; Low-Noise and RF Power Amplifiers for Telecommunication