



*A Guide to Learning the Testbench Language Features*

*Xilinx Spartan-3 Version*

*Guide to Language, Methodology and Applications*

*Digital Design: International Version*

SystemVerilog is a rich set of extensions to the IEEE 1364-2001 Verilog Hardware Description Language (Verilog HDL). These extensions address two major aspects of HDL based design. First, modeling very large designs with concise, accurate, and intuitive code. Second, writing high-level test programs to efficiently and effectively verify these large designs. This book, *SystemVerilog for Design*, addresses the first aspect of the SystemVerilog extensions to Verilog. Important modeling features are presented, such as two-state data types, enumerated types, user-defined types, structures, unions, and interfaces. Emphasis is placed on the proper usage of these enhancements for simulation and synthesis. A companion to this book, *SystemVerilog for Verification*, covers the second aspect of SystemVerilog.

*Embedded SoPC Design with Nios II Processor and VHDL Examples*

*The Verilog® Hardware Description Language*

*101 Common Coding Errors and How to Avoid Them*

*VERILOG HDL Quick Reference Guide*

*A Guide to Digital Design and Synthesis*